Abstracts

Unified CAD model of microstrip line with backside aperture for multilayer integrated circuit

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Microstrip line structure with backside aperture is developed for designing low-loss and high-impedance transmission line based multilayer integrated circuits. Applying a so-called "short-open calibration" (SOC) technique that is self-contained in our full-wave method of moments (MoM), we can formulate a unified equivalent CAD circuit model that is able to exhibit electrical properties of the proposed structure. We developed CAD model and structure are effectively used to design a multistage microstrip low-pass filter. Predicted filtering characteristics are well confirmed by our measurements.

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